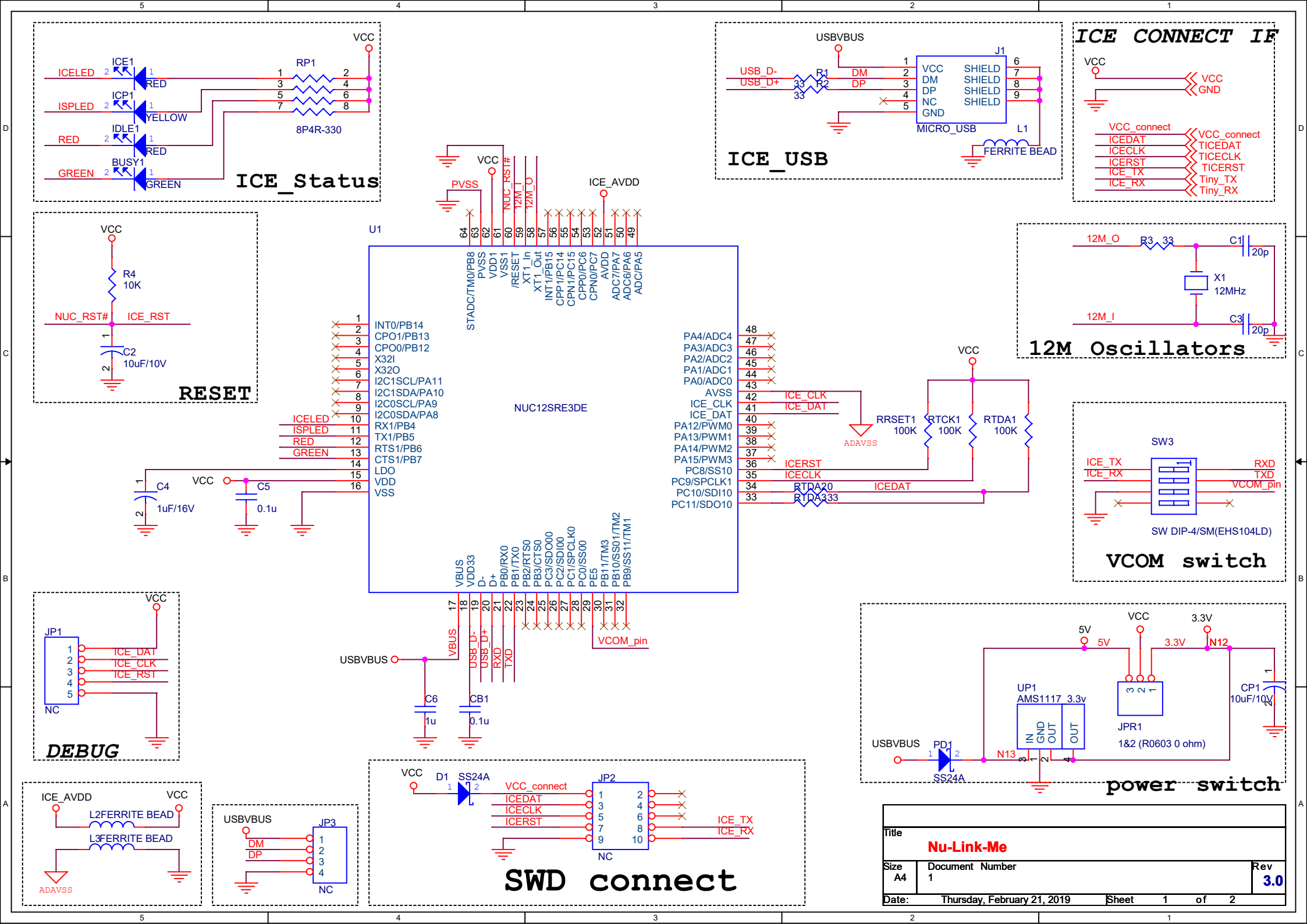
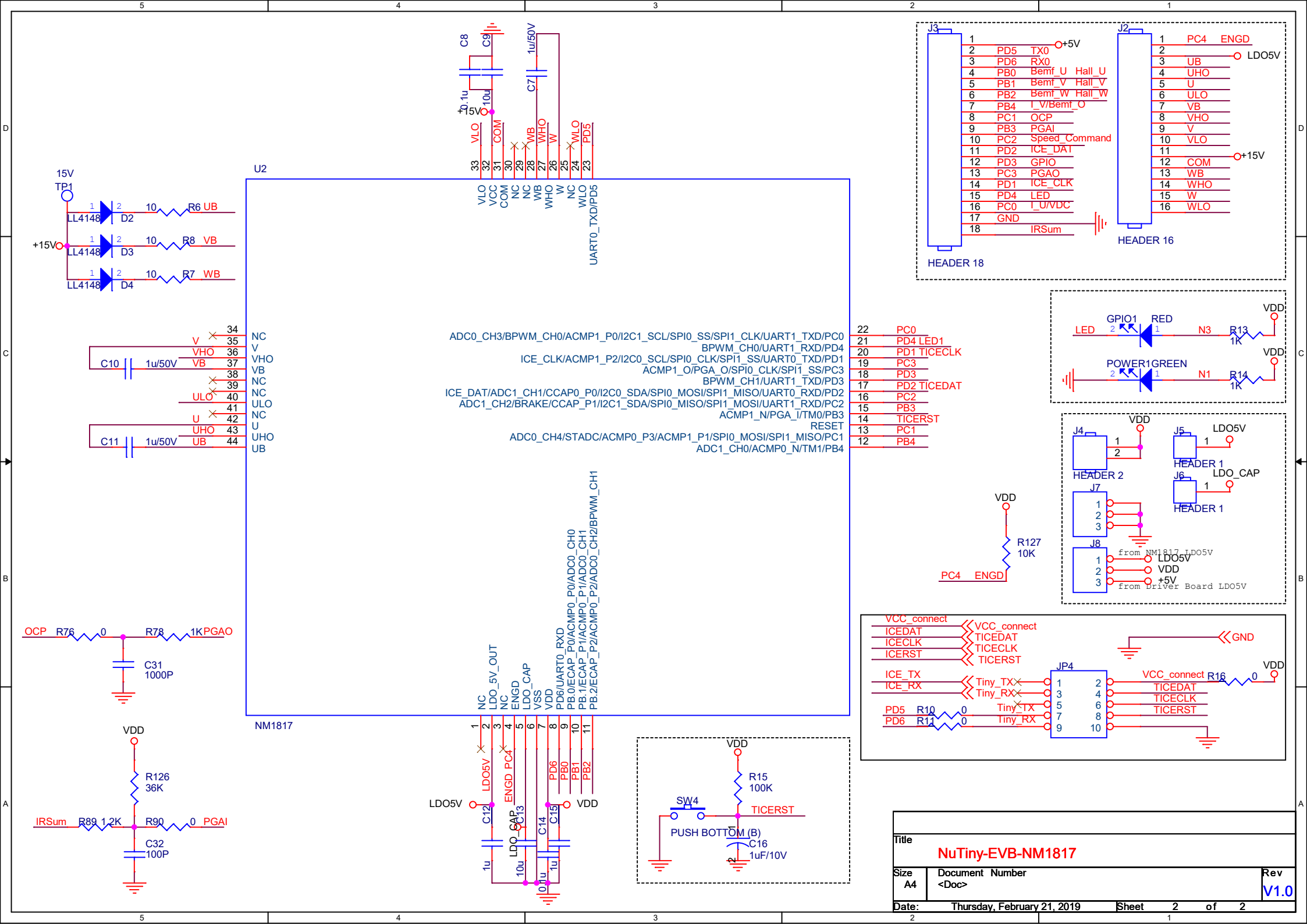

ICE_Status
ICE1
ICELED
RED
ICP1
ISPLED
YELLOW
IDLE1
RED
RED
BUSY1
GREEN
GREEN
RP1
8P4R-330
VCC
ICE_USB
USBVBUS
USB_D-
USB_D+
R1
R2
33
33
DM
DP
J1
VCC
DM
DP
NC
GND
SHIELD
MICRO_USB
L1
FERRITE BEAD
ICE CONNECT IF
VCC_connect
ICEDAT
ICECLK
ICERST
ICE_TX
ICE_RX
TICEDAT
TICECLK
TICERST
Tiny_TX
Tiny_RX
RESET
R4
10K
NUC_RST#
ICE_RST
C2
10uF/10V
U1
NUC12SRE3DE
PVSS
ICE_AVDD
STADC/TM0/PB8
PVSS
VDD1
VSS1
/RESET
XT1_In
XT1_Out
INT1/PB15
CPP1/PC14
CPN1/PC15
CPP0/PC6
CPN0/PC7
AVDD
ADC7/PA7
ADC6/PA6
ADC/PA5
INT0/PB14
CPO1/PB13
CPO0/PB12
X32I
X32O
I2C1SCL/PA11
I2C1SDA/PA10
I2C0SCL/PA9
I2C0SDA/PA8
RX1/PB4
TX1/PB5
RTS1/PB6
CTS1/PB7
LDO
VDD
VSS
ICELED
ISPLED
RED
GREEN
PA4/ADC4
PA3/ADC3
PA2/ADC2
PA1/ADC1
PA0/ADC0
AVSS
ICE_CLK
ICE_DAT
PA12/PWM0
PA13/PWM1
PA14/PWM2
PA15/PWM3
PC8/SS10
PC9/SPCLK1
PC10/SDI10
PC11/SDO10
ICERST
ICECLK
RTDA20
RTDA33
ICEDAT
ADAVSS
RRSET1
100K
RTCK1
100K
RTDA1
100K
C4
1uF/16V
C5
0.1u
12M Oscillators
12M_O
R3
33
C1
20p
X1
12MHz
12M_I
C3
20p
VCOM switch
SW3
ICE_TX
ICE_RX
RXD
TXD
VCOM_pin
SW DIP-4/SM(EHS104LD)
DEBUG
JP1
ICE_DAT
ICE_CLK
ICE_RST
NC
VBUS
VDD33
D-
D+
PB0/RX0
PB1/TX0
PB2/RTS0
PB3/CTS0
PC3/SDO00
PC2/SDI00
PC1/SPCLK0
PC0/SS00
PE5
PB11/TM3
PB10/SS01/TM2
PB9/SS11/TM1
VCOM_pin
USBVBUS
C6
1u
CB1
0.1u
power switch
5V
VCC
3.3V
N12
UP1
AMS1117 3.3v
IN
GND
OUT
OUT
JPR1
1&2 (R0603 0 ohm)
CP1
10uF/10V
USBVBUS
PD1
SS24A
N13
ICE_AVDD
L2FERRITE BEAD
L3FERRITE BEAD
ADAVSS
USBVBUS
JP3
DM
DP
NC
SWD connect
D1
SS24A
JP2
VCC_connect
ICEDAT
ICECLK
ICERST
ICE_TX
ICE_RX
NC
Title
Nu-Link-Me
Size
A4
Document Number
1
Rev
3.0
Date:
Thursday, February 21, 2019
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U2
NM1817
15V
TP1
LL4148 D2
LL4148 D3
LL4148 D4
R6 UB
R8 VB
R7 WB
C10 1u/50V
C11 1u/50V
C8 0.1u
C9 10u
C7 1u/50V
+15V
VLO COM WB WHO W WLO PD5
VLO VCC COM NC NC WB WHO W NC WLO UART0_TXD/PD5
NC V VHO VB NC NC ULO NC U UHO UB
ADC0_CH3/BPWM_CH0/ACMP1_P0/I2C1_SCL/SPI0_SS/SPI1_CLK/UART1_TXD/PC0
BPWM_CH0/UART1_RXD/PD4
ICE_CLK/ACMP1_P2/I2C0_SCL/SPI0_CLK/SPI1_SS/UART0_TXD/PD1
ACMP1_O/PGA_O/SPI0_CLK/SPI1_SS/PC3
BPWM_CH1/UART1_TXD/PD3
ICE_DAT/ADC1_CH1/CCAP0_P0/I2C0_SDA/SPI0_MOSI/SPI1_MISO/UART0_RXD/PD2
ADC1_CH2/BRAKE/CCAP_P1/I2C1_SDA/SPI0_MISO/SPI1_MOSI/UART1_RXD/PC2
ACMP1_N/PGA_I/TM0/PB3
RESET
ADC0_CH4/STADC/ACMP0_P3/ACMP1_P1/SPI0_MOSI/SPI1_MISO/PC1
ADC1_CH0/ACMP0_N/TM1/PB4
22 PC0
21 PD4 LED1
20 PD1 TICECLK
19 PC3
18 PD3
17 PD2 TICEDAT
16 PC2
15 PB3
14 TICERST
13 PC1
12 PB4
NC LDO_5V_OUT NC ENGD LDO_CAP VSS VDD PD6/UART0_RXD PB.0/ECAP_P0/ACMP0_P0/ADC0_CH0 PB.1/ECAP_P1/ACMP0_P1/ADC0_CH1 PB.2/ECAP_P2/ACMP0_P2/ADC0_CH2/BPWM_CH1
LDO5V C12 1u
ENGD PC4 C13 10u
LDO_CAP
C14 0.1u
C15 1u
PD6 PB0 PB1 PB2
VDD
OCP R76 0 R78 1K PGAO
C31 1000P
VDD R126 36K
IRSum R89 1.2K R90 0 PGAI
C32 100P
VDD R127 10K
PC4 ENGD
SW4 PUSH BOTTOM (B)
VDD R15 100K
TICERST
C16 1uF/10V
J3 HEADER 18
1 +5V
2 PD5 TX0
3 PD6 RX0
4 PB0 Bemf_U Hall_U
5 PB1 Bemf_V Hall_V
6 PB2 Bemf_W Hall_W
7 PB4 I_V/Bemf_O
8 PC1 OCP
9 PB3 PGAI
10 PC2 Speed_Command
11 PD2 ICE_DAT
12 PD3 GPIO
13 PC3 PGAO
14 PD1 ICE_CLK
15 PD4 LED
16 PC0 I_U/VDC
17 GND
18 IRSum
J2 HEADER 16
1 PC4 ENGD
2
3 UB LDO5V
4 UHO
5 U
6 ULO
7 VB
8 VHO
9 V
10 VLO
11
12 COM +15V
13 WB
14 WHO
15 W
16 WLO
GPIO1 RED LED N3 R13 1K VDD
POWER1 GREEN N1 R14 1K VDD
J4 HEADER 2 VDD
J5 HEADER 1 LDO5V
J6 HEADER 1 LDO_CAP
J7
J8 from NM1817 LDO5V LDO5V VDD +5V from Driver Board LDO5V
VCC_connect ICEDAT ICECLK ICERST
VCC_connect TICEDAT TICECLK TICERST
ICE_TX ICE_RX
Tiny_TX Tiny_RX
PD5 R10 0
PD6 R11 0
JP4
GND
VDD
VCC_connect R16 0
TICEDAT TICECLK TICERST
Title
NuTiny-EVB-NM1817
Size A4
Document Number <Doc>
Rev V1.0
Date: Thursday, February 21, 2019
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